

## **SPECIFICATION AMENDMENTS**

Please amend paragraph [0013] as follows.

Figure 8 is a high-level block diagram showing an eight-bank double-sided memory configuration according to an embodiment of the present invention; and

On page 4, please amend paragraph [0014] as follows.

Figure 9 is a high-level block diagram showing a communication network according to an embodiment of the present invention[[;]] .

On page 4, please amend paragraphs [0015] and [0016] as follows.

~~Figure 10 is a high level block diagram of a network processor according to an embodiment of the present invention; and~~

~~Figure 11 is a high level block diagram of a network device according to an embodiment of the present invention.~~